



***Report on Industrial visit to Department of Electronics and Communication Engineering Students to SEMICON India 2025.***

**Dated:** 3<sup>rd</sup> Sep.,2025  
**Department:** Electronics & Communication Engineering  
**Venue:** **SEMICON 2025, New Delhi**

**Objective:** To provide students with the exposure of latest technologies in the field of semiconductors, The **SEMICON India 2025**, the country's largest semiconductor conference visit is planned

**Activities:** The visit included a range of activities aimed at deepening students' understanding of the different aspects of semiconductor design, fab and display manufacturing, packaging, R&D, State policies, ecosystem development etc

**SEMICON India 2025** was jointly organized by the **India Semiconductor Mission (ISM)** under the Ministry of Electronics and Information Technology (MeitY) and **SEMI**, the global semiconductor industry association. This landmark event brought together **global industry leaders, policymakers, academia, companies and startups**, fostering investment, dialogue, and strategic partnerships. It served as a catalyst in advancing India's semiconductor ambitions by enabling **cross-border collaboration, commercialization of research, skill development, and integration into the global semiconductor value chain.**

**Following activities were included:**

**1.Registration and Exhibitor interaction:** The students registered for the event, then underwent visit for exhibitor's stalls. In the process students learn about the latest in semiconductor design and development. They were exposed to the latest chip design processes, innovations and industrial facilities

**2. Introduction to SEMI (Semiconductor Equipment and Materials International) university program:**

1. Students were made aware of technical and business training for professionals and newcomers to the global semiconductor industry.
2. Curriculum includes 800+ on-demand courses:  
Front-end and back-end manufacturing operations, Chip design principles, Technology trends Includes content from classroom events and webinars, Partnerships with content providers to deliver training quickly, Helps advance careers, grow businesses, contribute to the industry, and onboard new workforce

- 3. Interactive Panel discussions and conference: panel discussions and keynotes** focused on catalyzing India's **chip design ecosystem**, the event attracted wide interest from **business and technology leaders, researchers, industry analysts**, and professionals across the **microelectronics supply chain**—including **design engineers, equipment manufacturers, scientists, technicians, students**, and many others.

**Benefit to the Students from this Visit:**

Benefits of Industrial Visit can be summarized as follow: -

1. Inspire and motivate students by showcasing latest semiconductor industry
2. Provide students the in-depth corporate knowledge of the latest technology in the field of Design to fabrication in Chip design.
3. Gives the students a change to apply into actual practice the fundamentals that they learned in their curriculum.
4. A taste of working life for students who do not have a work experience. Helps student in deciding the kind of specialization they want to pursue.
5. Students can see how theoretical knowledge is translated into practical training programs and modules.
6. Get thorough insight into Industry Standards being practiced. Hands on Demonstrations of latest Technologies.
7. Will also enhance Interview preparation skills by talking to Industry experts.
8. Students learn about current industry trends, technologies, and skill requirements directly from professionals.
9. Students learned about IC design training programs and the impact they have on skill development.
10. Students interacted with the world leaders in IC design.

**Faculty Member:** Dr. Sanjay Sharma

**Photographs:**









