

Dr. Akhilesh Das Gupta Institute of Technology & Management

Report on “SCOPE AND OPPURTUNITIES IN THE FIELD OF VLSI DESIGN BY ALUMNI SHUBHAM GARG”

Dated: 11/04/2023
Department: Electronics and Communication Engineering
Venue: Online Session
Objective: This program aimed to have a talk regarding scope and opportunities in the field of VLSI Design.

Activities:

The Seminar started with a warm welcome by Dr Niranjan Bhattacharya (HOD) and Ms Richa Malhotra (Event In charge) followed by a brief introduction of the Alumni Speaker Mr Shubham Garg.

The speaker unfolded the seminar by conveying how he started his journey, successfully published 4 research papers, got interviewed by the Hindustan Times newspaper, has been a mentor at UNESCO India Africa Hackathon (UAIH-2022) and is currently pursuing a master's at C-DAC Noida and Thesis at IIT Guwahati.

Starting with the quote 'I believe in making the world a collaborative place rather than Competitive' he addressed some crucial topics such as the Indian Semiconductor mission, FABLABS and how there will be a boom for the VLSI industry in the coming future.

He also talked majorly about chip development and how there are some really good programs released by the Indian government to support it. He also made the students glance at the chips he developed and also talked about RISC-V and nanometer technology.

Not to forget to mention that he also mentioned how India can become a semiconductor hub and also stated the fact that there is so much scope in VLSI System design and blockchain collectively.

By wrapping up the session he concluded that we must try to implement knowledge in which ever technology we are invested in by making projects and presenting in various hackathons and competitions.

In the end, it must be concluded that it was worthwhile to attend the session. The students benefited much from it.

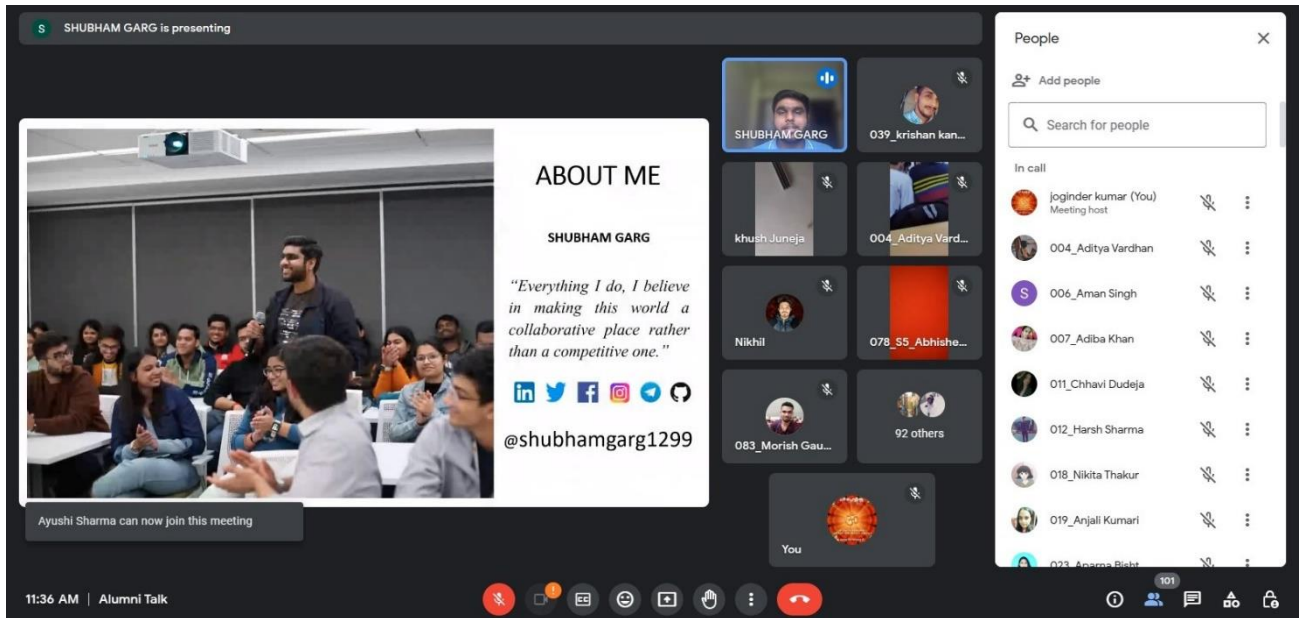
Aim of the Mentoring program:

1. To give the exposure of pursuing higher studies.
2. To give an idea about the VLSI System design to the students.

Outcomes: Students interacted with the speakers and cleared their doubts.

- Beneficiaries:**
1. Student of ECE Department
 2. About one hundred students have attended the talk.

Photographs:



INTRODUCING INDIA'S INDIGENOUS MICROCONTROLLER DEVELOPMENT BOARD

ARIES Micro

Microprocessor Development Programme, Initiated and funded by MeitY, Govt. of India

SHUBHAM is presenting

PICORV-32a

SHUBHAM is presenting

VEGA MICROPROCESSORS

VEGA ET1031
32-bit 3-stage In-Order Single Core

VEGA AS1061
64-bit 6-Stage In-Order Single Core

VEGA AS1161
64-bit 16-Stage Out-of-Order Single Core

VEGA AS2161
64-bit 16-Stage Out-of-Order Dual Core

VEGA AS4161
64-bit 16-Stage Out-of-Order Quad Core

SHUBHAM GARG is presenting

SHUBHAM GARG

039_krishan kan...

Kishan Singh

083_Morish Gau...

Nikhil

khush Juneja

Rishabh Kashyap...

92 others

You

024_Harkirat Singh has left the meeting

11:46 AM | Alumni Talk